

IP-Core SMPTE2022-1/2 TS over IP

The M3L IP-Core SMPTE2022-1/2 TS over IP is based on SMPTE2022-1 and SMPTE-2 standards. When receiving network packets, this core uses the Real-time Transport Protocol (RTP) including timestamp for clock recovery.

This core has burst error correction code blocks, LFEC and DFEC, in order to correct the receiving data on occasion of packet loss or packet reordering in the network.

This IP-Core has two types : one is a transmitter(vEGx4) and the other a receiver(vIGx4). You can use this core in various way. Supporting a faster line than a 1GbE line is optional.

Features	vEGx1	vIGx1
1. MPEG-TS Over IP transmitting	○	—
2. MPEG-TS Over IP receiving	—	○
3. Using UDP/RTP protocol based on SMPTE 2022-2-2007	○	○
4. Error correcting function based on SMPTE-2022-1-2007	○	○
5. MAC/IP/UDP/RTP filtering	—	○
6. Supporting IPv4. IPv6 support is optional.	○	○
7. ARP support is Optional.	○	○
8. Supporting TTS(TS with a timestamp)	○	○

