

IP\_SMPTE2110

The IP\_SMPTE2110(SV1) of our Video-over-IP core family is an IP core conforming to SMPTE ST 2110-10, -20, -30, and -40. The IP\_SMPTE2110 series are composed of 2 types of cores, a transmission functionality of vEGSV (Egress-Core) and a receive functionality of vIGSV (Ingress-Core), it can be used with a single, multiple, or combination of cores.

In addition, the number of SDI channel and ethernet ports are customizable with your requirements.

Features	
1.	Compliant to SMPTE ST2110-10, -20, and -30 (to be ready for -40 after fixed its standard spec)
2.	Video format for 4K, HD, and SD. (Quad-link of 3G-SDI for 4K/12Gbps video can be sent and received via 2 channels of 10 GbE)
3.	Ethernet port compatible with 10 GbE, 25 GbE, 40 GbE (4 x 10 GbE), 100 GbE (4 x 25 GbE)
4.	Optional for SMPTE ST2022-7 (Hitless) (It can be implemented with double packets on 1 line, although it is normally achieved with 2 lines)
5.	Optional for in-channel communication of control packets (Optional for the hitless with IP_SMPTE2059, clock recovery based on time synchronization)
6.	Compatible for IP v4 and/or IP v6
7.	Optional for ARP
8.	Management registers for Ethernet statistics (number of receiving packets and loss packets)

